

Faculty Profile



Dr. Satya Gopal Dinda, BE, M.Tech, Ph.D

Associate Professor

Date of Joining: 01.06.2024

HIGHLIGHTS:

- Number of Journal Publications :6
- H-Index :2
- Project Funding Received : Nil
- Patents Published : Nil
- Patents Granted : Nil

PROFESSIONAL LINKS:

- Scopus ID : 55170460900
- Scopus Link :<https://www.scopus.com/authid/detail.uri?authorId=55170460900>
- Google Scholar ID : 6JDohjoAAAAJ
- Anna University Faculty ID : 2122738
- AICTE Faculty ID :1-9509065422
- Anna University Supervisor ID : NA
- LinkedIn : <https://www.linkedin.com/in/satya-dinda-505563237/>

PROFESSIONAL BACKGROUND:

- Teaching Experience till date : 7years 8 Months
- Industrial Experience : Nil

INTERNATIONAL EXPOSURE:

- NA

AREA OF SPECIALIZATION:

- Physics of Semiconductor Devices,
- Nanoelectronics,
- Semiconductor Device Modeling,
- Analog & Digital Electronics and Circuits,
- VLSI Circuits & Systems,
- Low Power VLSI Design Techniques
- **Research Interests:** 1. Nanoelectronics, Compact modeling of advanced CMOS device 2. Novel semiconductor device simulation and modeling, 2D material. 3. Low power VLSI systems 4. IC Fabrication & Testing. 5. Quantum Computing

Ph.Ds AWARDED WITH DETAILS:

S:No	Name of Scholar	Title of Thesis	Year of Completion	Full Time/Part Time
	NA			

Ph.Ds GUIDING:

- Full Time Scholars : NA
- Part Time Scholars : NA

SPONSORED RESEARCH / FUNDING APPLIED / CONSULTANCY:

- NA

PATENTS PUBLISHED/GRANTED:

- NA

COURSES CERTIFIED:

- System Design Through Verilog [NPTEL]
- Physics of Nano Scale Device [NPTEL]

ACHIEVEMENTS AND AWARDS:

1. GATE percentile : 90.28% Paper : EC Year : 2006
2. Institute Assistantship Award by IIT Kanpur, Year : 2010
3. Set the university question paper in the year 2022 and 2023. The Maulana Abul Kalam Azad University of Technology, West Bengal conducts B.Tech examination in more than 150 affiliated engineering college.
4. Topper in “System Design Through Verilog” conducted by NPTEL, IIT Madras in 2023
5. Topper in “Physics of Nano Scale Device” conducted by NPTEL, IIT Madras in 2023

SPECIAL SESSIONS DELIVERED:

1. Delivered a seminar as a **speaker** at Saveetha Engineering College, Chennai on 25 October, 2024. Seminar Title: *20 Treasures of a Promising Engineer.*
2. Organized seminar as a **coordinator** at Saveetha Engineering College, Chennai on 26 April, 2024. Seminar Title: *Decoding VLSI design from RTL2GDS.*
3. Organized seminar as a **coordinator** at Saveetha Engineering College, Chennai on 28 March, 2024. Seminar Title: *An Insight into The Fair Deep Learning.*
4. Organized seminar as a **coordinator** at Saveetha Engineering College, Chennai on 2 March, 2024. Seminar Title: *Current Perspectives of VLSI Circuit Testings.*
5. Organized seminar as a **coordinator** at Budge Budge Institute of Technology, Kolkata on 29 March, 2023. Seminar Title: *Some Perspective of Solar Photovoltaic Technology.*

CONFERENCES/SEMINARS/WORKSHOPS ATTENDED:

1. Five days Workshop program attended on **“Challenges in VLSI: cutting edge perspectives”** from 21/07/2008 to 25/07/2008 at **Indian Institute of Engineering Science & Technology Shibpur**. Status: Certificate received
2. Attended **International Workshop on the Physics of Semiconductor Devices (IWPSD 2011)** from 19/12/2011 to 21/12/2011 held at **IIT Kanpur**. Status : Certificate received
3. Faculty development program attended on **“Nanoelectronics Devices: Materials to Applications”** from 06/12/2021 to 10/12/2021 at **Indian Institute of Information Technology Ranchi**. Status: Certificate received
4. Participated the FDP on **“Scilab Programme for Engineering Application”** from 08/05/2023 to 12/05/2023 conducted by the **NITTR Chandigarh**. Status : Certificate received
5. Five days Industrial Workshop program attended on **“VLSI to System design: Silicon to end Application Approach”** from 31/07/2023 to 04/08/2023 organized by **AICTE, ARM Education and ST Microelectronics**. Status: Certificate received
6. Participated the FDP on **“ECE Lab Practices using Multisim Live Online Simulator”** from 4/12/2023 to 8/12/2023 conducted by the **NITTR Chandigarh**. Status : Certificate received
7. Participated the online FDP on **“Quantum Computing: Algorithms and Applications”** from 15th July, 2024 to 19th July, 2024 conducted by the **NIT Warangal**. Status : Certificate received

PROFESSIONAL MEMBERSHIPS:

- IEEE Member ID: 90923969

OTHER DETAILS (If any):

- Member of project execution team of the SMDP - C2SD scheme from May, 2018 to May, 2019 at MANIT Bhopal.
- Acted as a library in-charge in the department of ECE, BBIT Kolkata from June 2021 to June, 2022.
- Acted as a laboratory in-charge of analog electronics circuit laboratory, BBIT Kolkata from June 2021 to June, 2022.