

Faculty Profile



Dr E Poovannan, M.Tech., Ph.D

Assistant Professor

Date of Joining: 13.06.2024

HIGHLIGHTS:

- Number of Journal Publications:05
- H-Index:2
- Patents Published:5

PROFESSIONAL LINKS:

- Scopus ID: 55616165100
- Scopus Link: <https://www.scopus.com/authid/detail.uri?authorId=55616165100>
- Anna University Faculty ID: 272070
- AICTE Faculty ID: 1-9381049721

PROFESSIONAL BACKGROUND:

- Teaching Experience till date: 14 years
- Industrial Experience: NIL

AREA OF SPECIALIZATION:

- VLSI Design
- Machine Learning

PATENTS PUBLISHED/GRANTED:

| S.No. | Title | Publication Date & Page No | Status |
|-------|---|-------------------------------|-----------|
| 1. | Method for Utilizing Machine Learning for Power Prediction in VLSI Circuits | 28/10/2022 202241061426 | Published |
| 2. | Biomimetic Swarm Intelligence For Autonomous Cybersecurity Mesh Networks. | 05/07/2024 202441050314 A | Published |
| 3. | Generative adversarial network (gan)-based synthetic data generation for privacy preserving Machine learning. | 02/08/2024, 202441056350 A | Published |

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| 4. | Explainable AI System with Interactive Visualization of Decision-Making Processes | 31/08/2024, 202441066014 A | Published |
| 5. | Infrared projector for vein Detection | 11/02/25 Design No. : 436455-001 | Published |
| 6. | Blockchain-Integrated Machine Learning Framework For Secure Data Transactions | 21.02.25, 202541013217 A | Published |
| 7. | Machine Learning Algorithms For Adaptive Consensus Mechanisms In Blockchain Networks | 21.02.25, 202541013226 A | Published |

COURSES CERTIFIED:

- Completed an online course on Machine Learning Practical in Coursera.
- Completed an online course on Practical Introduction to fuzzy logic using MATLAB in Udemy
- Digital Electronics –NPTEL
- VLSI Design -NPTEL

ACHIEVEMENTS AND AWARDS:

- SRM University awarded 'Best Paper' in 2013.
- Awarded 'Best Class In-charge' by SRMIST, Ramapuram in 2019.

CONFERENCES/SEMINARS/WORKSHOPS ATTENDED:

International Conference:

- E Poovannan, S karthik, “Importance of Machine Learning in IC Design”, Proceedings of the 17th INDIACom; INDIACom-2023; IEEE Conference ID: 57626 2023 10th International Conference on “Computing for Sustainable Global Development”, 15th - 17th March, 2023 Bharati Vidyapeeth's Institute of Computer Applications and Management (BVICAM), New Delhi (INDIA)
- Presented a paper in International Conference on Innovative Trends in Engineering and Technology [ICITET-2022] organized by K J College of Engineering & Management Research in Kondhwa Annexe, Pune – 48, India, 28th & 29th July 2022.(Paper Title: Analysis of a Full Subtractor in Cadence 45nm Technology Utilising Various Method)
- Presented a paper in International Conference on Contemporary Challenges in Management, Education, Engineering & Applied Sciences [IC3MEEAS – 2022] organized by CHERAN COLLEGE OF ENGINEERING in Karur,TN, India, 30th July 2022 (Paper Title: power estimation techniques in VLSI Circuits)
- Presented a paper in an International Conference titled “International Conference on Research in Electronics Engineering and Communication Techniques” (ICREACT 2021) conducted by SRM institute of Science and Technology, Vadapalani on 28.may 2021. (Paper Title: Artificial Intelligence in Physical Design of Semiconductors).
- Presented a paper in international conference titled on “**Implementation of MPLS Layer 2 VPN in Ipv6 Network** ” KCG college of Engineering on March’2020

- Presented a paper in international conference titled on “ A Low Power Clock Distribution system using a current pulsed clocked Flip Flop” Saveetha School of Engineering, Chennai on April 2019.

Faculty Development Program & Workshop Attended:

- Participated in SIX DAYS BOOTCAMP on "DESIGN THINKING" held from 15th July 2024 to 20th July 2024, organised by the Department of Electronics and Communication Engineering, SRM Institute of Science and Technology, Vadapalani, Chennai.
- Participated & completed AICTE Training And Learning (ATAL) Academy Faculty Development Program on Integrating Machine Learning and Deep Learning in Real-time IoT Systems (in Association with NVIDIA) at SAVEETHA ENGINEERING COLLEGE from 02/12/2024 to 07/12/2024.
- Participated in FDP Program on “Insight of Analog and Digital IC Design overview of VLSI Tools” from 14.11.2022 to 19.11.2022 organized by the Department of Electronics and Communication Engineering, SRM Institute of Science and Technology, Kattankulathur Campus.
- Participated in the STTP on "Connecting People to the Semiconductor Industry", from 05.12.22 to 17.12.2022 organized by ECE Department, SRM Institute of Science and Technology, Vadapalani.
- Participated in FDP on Machine Learning Techniques In VLSI Design conducted by ATAL (AICTE Training and Learning) at MS Ramaiah Institute Of Technology from 26.07.2021 to 30.07.2021.
- Participated in “ISTE Sponsored Short Term Training Programme on Deep Learning Tools and its Applications” during the period 25th to 29th July, 2022 conducted by Sri Sairam Engineering College, Chennai-44.
- Participated in “Webinar on Data Science & its Applications” conducted by BVICAM, New Delhi on 20th August, 2022
- Participated in a webinar on “HOW TO DO QUALITY PUBLICATIONS” conducted by the Department of Computer Science & Engineering, AALIM MUHAMMED SALEGH COLLEGE OF ENGINEERING on 27th August, 2022.
- Participated in the six days virtual workshop on "Research Opportunities in Semiconductor Materials and Devices (ROSMD-2022)" during October 19-24, 2022 organized by the Department of Electronics and Communication Engineering, SRM Institute of Science and Technology, Kattankulathur Campus in association with the Indian Institute of Information Technology, Design and Manufacturing, Kancheepuram.
- Attended Five days Faculty Development Programme on “Machine Learning for Multidisciplinary Fields”, Organized by the Department of Data Science and Business Systems, SRMIST, KTR in association with RAISE Center of Excellence powered by MIT Square, London from 27.10.2022 to 31.10.2022.
- Attended Webinar on ‘AI in Healthcare and Medicine’ on 19 November, 2022, organized by BVICAM, New Delhi.
- Participated in FDP Program on “Applications of Artificial Intelligence in Engineering” from 08.08.2022 to 19.08.2022 organized by E&ICT Academy, NIT Warangal.
- Participated in FDP Program on “Insight of Analog and Digital IC Design: overview of VLSI Tools” from 14.11.2022 to 19.11.2022 organized by the Department of Electronics and Communication Engineering, SRM Institute of Science and Technology, Kattankulathur Campus.

PROFESSIONAL MEMBERSHIPS:

- Member of IEEE: 98149728