

Faculty Profile



Dr. A.N.Duraivel, M.E., Ph.D.

Associate Professor / ECE

HIGHLIGHTS:

- Number of Journal Publications: 14
- H-Index: 3
- Patents Published: 2
- Patents Granted: 3

PROFESSIONAL LINKS:

- Scopus ID: [57226884787](https://www.scopus.com/authid/detail.uri?authorId=57226884787)
- Scopus Link: <https://www.scopus.com/authid/detail.uri?authorId=57226884787>
- Google Scholar ID:
https://scholar.google.com/citations?hl=en&user=bduAml8AAAAJ&view_op=list_works&sortby=pubdate
- Anna University Faculty ID: 2673721981
- AICTE Faculty ID: 1-453109516
- Anna University Supervisor ID: 4190049
- LinkedIn:

PROFESSIONAL BACKGROUND:

- Teaching Experience till date: 22 Years
- Industrial Experience: NIL

INTERNATIONAL EXPOSURE:

- Organized International Conference ICICET (“International Conference on Innovations and Challenges in Engineering and technology-2024 (ICICET-2024)” held on 10th May-2024 at Kings Engineering College.

AREA OF SPECIALIZATION:

- **VLSI Design,**
- Biomedical and Image Processing,
- Wireless Sensor Networks,
- Embedded Systems and IoT,
- Machine Learning.

Ph.Ds AWARDED WITH DETAILS: Nil

Ph.Ds GUIDING:

- **Full Time Scholars:** Nil
- **Part Time Scholars :** 4

SPONSORED RESEARCH / FUNDING APPLIED / CONSULTANCY: Nil

PATENTS PUBLISHED/GRANTED:

1. Patent Published titled, “Ai-Assisted High Resolution Image Reconstruction For Ultrasound And MRI”, with the Application Number 202541119486 A published on 19/12/2025.
2. Design patent grant titled, “Artificial Intelligence (Ai) Based Cloud Security Detecting Device” with the Design number 429016-00 issued on 21-11-2024.
3. Design patent grant titled, “Network Adapter with On-Chip Hybrid Buffer Capacity” with the Design Application number 429920-001 issued on 22-10-2024.
4. Design patent grant titled, “AI Enabled Consumer Behaviour based self-Adjusting KIOSK” with the Design Application number 415192-001 issued on 13-06-2024.
5. Patent Published titled, “Disease Identification System Through Ocular Diagnosis” with the Application Number 201941005618 A published on 22-02-2019.

COURSES CERTIFIED:

- Completed NPTEL online certification course “**Integrated Circuits and Applications**” and received certificate with **Elite** grade and consolidated score 64% during Jan-Apr 2025.
- Completed NPTEL online certification course “**Fundamentals of Semiconductor Devices**” and received certificate with consolidated score 52% during Jan-Apr 2025.

- Completed NPTEL online certification course “**Microprocessors and Interfacing**” and received certificate with **Elite** grade and consolidated score 63% during Jan-Apr 2024.
- Completed NPTEL online certification course “**Analog Circuits**” and received certificate with **Elite+Silver** grade and consolidated score 76% during Jan-March 2024.
- Completed NPTEL online certification course “**Microprocessors and Microcontrollers**” and received certificate with **Elite** grade and consolidated score 64% during Jan-Apr 2023.
- Completed NPTEL online certification course “**Digital Circuits**” and received certificate with **Elite+Silver+ Topper 1%** grade and consolidated score 83% during July-Oct 2022.
- Completed NPTEL online certification course “**Digital Electronic Circuits**” and received online certificate with **Elite** grade and consolidated score 68% during Jan-Apr 2022.
- Completed NPTEL online certification course “**System Design through VERILOG**” and received certificate with **Elite+Silver** grade and consolidated score 82% during July-Sep 2021.
- Completed NPTEL online certification course “**CMOS Digital VLSI Design**” and received online certificate with **Elite** grade and consolidated score 63% during Feb-Apr 2019.
- Completed NPTEL online certification course “**Switching circuits and Logic Design**” and received certificate with **Elite** grade and consolidated score 79% during July-Aug 2018.

LIST OF PUBLICATIONS:

Scopus ID: 57226884787

- 1) “**Efficient Resource Allocation in Cloud-Based IIOT Through Secure Energy-Aware High-Order Optimal Density Selection Using Supervised Attention-Based Bi-directional Long Short-Term Memory and Decision Tree-Based Salp Swarm Optimization**”, in the Journal SN Computers (A Springer Nature) – **Scopus Indexed**, Volume-7, Article 321, , Published on 28th March 2026, ISSN: 2661-8907.
DOI: <https://doi.org/10.1007/s42979-026-04900-1>
URL: <https://link.springer.com/article/10.1007/s42979-026-04900-1>
- 2) “**Energy-Efficient QSD Multiplier with High-Speed Binary-to Radix-4 Recoding for Solid State Biosensor Signals**”, in the ECS Journal of Solid State Science and Technology, Volume No-15, No-3, 9 March 2026, **Annexure-1 (SCIE-Regular Issue)** E ISSN: **2162-8777**
DOI: [10.1149/2162-8777/ae46de](https://doi.org/10.1149/2162-8777/ae46de)
URL: <https://iopscience.iop.org/article/10.1149/2162-8777/ae46de>
- 3) “**Cross-Technology Evaluation of a Gate-Diffusion-Input Magnitude Comparator for Scalable CMOS Architectures**”, in the International Journal Of Advances In Signal And Image Sciences (IJASIS) Open Access. Volume No-12, No-1, January 2026, PP 104-123, **Scopus Indexed (Yet to Index)**, E ISSN: **2457-0370**.
URL: <https://xlescience.org/index.php/IJASIS/issue/view/28>

- 4) **“Hybrid Deep Stacking Ensemble for Accurate Fruit Disease Classification in Precision Agriculture”**, in the Journal SN Computers (A Springer Nature) – **Scopus Indexed**, Volume–6, Article 1019, , Published on 12th December 2025, ISSN: 2661-8907.
DOI: <https://doi.org/10.1007/s42979-025-04586-x>
URL: <https://link.springer.com/article/10.1007/s42979-025-04586-x>
- 5) **“Design and FPGA implementation of information secured 3D HAAR wavelet and embedded zero tree encoding for image compression”**, in the “Information Security Journal: A Global Perspective”, **Volume-, No.-, June 2025, Scopus Indexed**, ISSN: 1939-3555 (Print) 1939-3547 (Online),
DOI: <https://doi.org/10.1080/19393555.2025.2503435>
URL: <https://www.tandfonline.com/doi/full/10.1080/19393555.2025.2503435>
- 6) **“Intelligent Public sanitation: An IoT based system for environmental sensing and predictive cleaning”** in the “ IEEE International Conference on Modern Sustainable Systems (CMSS-2025)” (**Scopus indexed**), held at Universiti Teknologi MARA, Malaysia in association with RVS College of Engineering and Technology, India(Online Mode). During 12th to 14th August-2025.
- 7) **“An Efficient Nano Scale Sequential Circuits with Clock Inherent Capability In QCA For Fast Computation Paradigm”**, in the International Journal of Computational and Experimental Science and Engineering (IJCESN)”, Volume-11, No.-1, January 2025, PP 364-372, **Scopus Indexed**, ISSN: 2149-9144.
DOI: <https://doi.org/10.22399/ijcesen.840>
URL: <https://ijcesen.com/index.php/ijcesen/article/view/840>
- 8) **“ASIC Design and Implementation of 32 Bit Arithmetic and Logic Unit”** in the Journal of EAI Endorsed Transactions on Energy Web, pp-1-7, Volume-11, August 2024, **Scopus Indexed**, ISSN: 2032-944X.
DOI: 10.4108/ew.6035
URL: <https://eudl.eu/doi/10.4108/ew.6035>
- 9) **“A Hybrid Genetic Algorithm and Neural Network-Based Cyber Security Approach for Enhanced Detection of DDoS and Malware Attacks in Wide Area Networks”**, in the Journal of Cybersecurity and Information Management (JCIM)”, Volume-14, Issue-2, July 2024, PP 253-263, **Scopus Indexed**, ISSN: 2769-7851 (Print), 2690-6775 (Online),
DOI: <https://doi.org/10.54216/JCIM.140217>.
URL: <https://americaspg.com/articleinfo/2/show/3029>
- 10) **“An efficient reconfigurable FIR Filter design with coefficients optimization using a modified Bacterial foraging optimization Algorithm”**, in the Journal Automatika, Volume-65, Issue-1, January 2024, PP 290-303, **Annexure-1 (SCI-Regular Issue) (Scopus Indexed)** ISSN: 0005-1144 (Print), 1848-3380 (Online). DOI: 10.1080/00051144.2023.2296792
<https://doi.org/10.1080/00051144.2023.2296792>
- 11) **“Computational framework for prediction of cardiac disorders by analyzing ECG signal Using Machine Learning Technique”**, in International Journal for Multiscale Computational Engineering, Volume-22, Issue-5, November 2023 **Annexure-1(SCI-Regular Issue) (Scopus Indexed)** ISSN: 1543-1649 (Print), 1940-4352 (Online). <https://doi.org/10.1615/IntJMultCompEng.2023050106>
<https://www.dl.begellhouse.com/journals/61fd1b191cf7e96f.617c618f41e0bb18.7885156049790961.html>

- 12) “FPGA based Silicon on Chip using Flexible Digital Pre-Distortion for Software Defined Radio Transceiver” in **Proceedings of the Sixth International Conference on Computing Methodologies and Communication (ICCMC 2022)**, **Scopus Indexed**, IEEE Xplore Part Number: CFP22K25-ART; ISBN: 978-1-6654-1028-1, DOI: [10.1109/ICCMC53470.2022.9754081](https://doi.org/10.1109/ICCMC53470.2022.9754081)
- 13) “Equalization of Finite-Alphabet MMSE for All-Digital Massive MU-MIMO mm-Wave Communication”, in **Journal of Physics: Conference Series**, 2021, (**Scopus Indexed**). <https://iopscience.iop.org/article/10.1088/1742-6596/1964/6/062049>
- 14) “Proficient Technique for High Performance Very Large-Scale Integration System to Amend Clock Gated Dual Edge Triggered Sense Amplifier Flip-Flop with Less Dissipation of Power Leakage”, in **Journal of nanoelectronics and optoelectronics**, volume 16, number 4, April-2021, PP 602–611, **Annexure-1 (SCI-Regular Issue)**, ISSN- 1555-130X. <https://www.ingentaconnect.com/content/asp/jno/2021/00000016/00000004/art00014>

Book Chapter - Renewable Energy with IoT and Biomedical Applications - VLSI Presents a Cost-Effective Micro Control Module With an Asymmetric Wireless Body Sensor Network Protection – PP:138-142. August-2021. [View article \(google.com\)](#)

Technical Text Books Published:

1. Published technical text Book titled, “**Linear Integrated Circuits**”, in Charulatha publication with ISBN: 978-93-5577-879-6.
2. Published technical text Book titled, “**VLSI and CHIP DESIGN**”, in Scientific International Publishing House with ISBN: 978-93-5757-599-7.
3. Published technical text Book titled, “**Digital Signal Processing and System design**”, in Scientific International Publishing House with ISBN: 978-93-5757-831-8.

ACHIEVEMENTS AND AWARDS:

- **ATAL FDP Coordinator:** Received Fund of **Rs.3.5Lakhs from AICTE to organize offline ATAL FDP** with the title, “**AI in the Development of Next-Generation Semiconductor Materials**”, During 18-06-2025 to 23-06-2025 in the department of Electronics and Communication Engineering at Kings Engineering College.
- Established **Anna University Recognized Research Centre** in the Department of Electronics and Communication Engineering, Kings Engineering College, Chennai.
- Received Certificate of Appreciation from NPTEL - recognized as “**NPTEL Domain STAR – VLSI DESIGN**” during Jan-2025 to Apr-2025.
- Received Certificate of Appreciation from NPTEL-Electrical Engineering for being recognized as “**NPTEL Discipline STAR**” during Jan-2023 to Apr-2023.

- Received Certificate of Appreciation from SIMATS Engineering for the contribution as a **session chair for judging the projects** demonstrated @ Demo Day2K23 – Future Innovation organized Department of wireless Communication, SIMATS on 24-03-2023.
- Received **Senior Research fellow AWARD** for the best contribution in Academic as well as Research and Development activity in International Conference on Emerging Trends in Science, Engineering and Management (ICETSEM-2021) from January 21st to 23rd -2021, Organized by the International Journal of MC Square Scientific Research, Chennai.

SPECIAL SESSIONS DELIVERED: Nil

CONFERENCES/SEMINARS/WORKSHOPS ATTENDED:

Conference:

- Participated and presented a paper entitled **“Intelligent Public sanitation: An IoT based system for environmental sensing and predictive cleaning”** in the “ IEEE International Conference on Morden Sustainable Systems (CMSS-2025)” (**Scopus indexed**), held at Universiti Teknologi MARA, Malaysia in association with RVS College of Engineering and Technology, India(Online Mode). During 12th to 14th August-2025.
- Received Certificate of **appreciation for Serving as reviewer** for IEEE Sponsored international conference on Data Science and Business system held on 17th and 18th April-2025, at SRM University KTR Campus, Chennai.
- Participated and presented a paper entitled **“Design and Performance Analysis of Reconfigurable Approximate Carry Look Ahead Adder using Modified Booth multiplier”** in the “International Conference on Transforming Engineering Systems for Sustainability (TESS-2023)”, held at Taylor’s University, Kuala Lumpur, Malaysia (Online Mode) on 29th to 30th Nov-2023.
- Participated and presented a paper entitled **“ PCB Defect detection Using FPGA”** in the “International Conference on Recent trends in Computing and Communication Engineering (ICRTCCE-2023)”, held at SIMATS Engineering, Chennai held on 17th March 2023.
- Participated and presented a paper entitled **“Portable Virtual Fencing and Tracking Belt for Toddlers”** in the “International Conference on Recent trends in Computing and Communication Engineering (ICRTCCE-2023)”, held at SIMATS Engineering, Chennai held on 17th March 2023.
- Participated and presented a paper entitled **“Fire Alarming System and Security Monitoring System”** in the “National Conference SILICON’23 on Virtual Reality

Technology in Cognitive Applications”, held at Dr.M.G.R. Educational and Research Institute, Chennai held on 27th-28th February 2023.

- **“FPGA based Silicon on Chip using Flexible Digital Pre-Distortion for Software Defined Radio Transceiver”**, 6th International Conference on Computing Methodologies and Communication (ICCMC-2022), IEEE Proceedings held at Surya Engineering College.
- Participated and presented a paper entitled **“Design of a Smart Safety Device for Women Using IOT”** in the “International Virtual Conference on Research Innovation in Science, Engineering and Technology (IVCRASET-2022)”, at Kings engineering college held on 1st & 2nd March-2022.
- Participated and presented a paper entitled **“Low Power and Area efficient approximate parallel design using Bypassing”** in the “International Conference on Recent trends in Computing, Communication and Networking Technologies (ICRTCCNT’19)”, held at Kings engineering college held on October 18-19-2019.
- Participated and presented a paper titled **“An IoT Based Fire Alarming and authentication System for Workhouse Using Raspberry PI 3”** in the “International Conference on Innovations and Challenges in Engineering and Technology-18” held at Kings engineering college held on 7th April 2018.
- Participated and presented a paper titled **“Implementation of Breakneck Self timed Adder”** in the “International Conference on Innovations and Challenges in Engineering and Technology-17” held at Kings engineering college held on 8th April 2017.
- Participated and presented a paper titled **“FPGA Implementation of High Speed Self timed Adder”** in the National Conference on Recent trends & Emerging Technologies, held at Rajiv Gandhi College of Engineering, Sriperumbudur, held on 24th March-2017.
- Participated and presented a paper titled **“Computerized fritter Management System”** in the National Conference on Communication and Computing, held at SRM University, Ramapuram on 2nd March-2017.
- Participated and presented a paper titled **“Web Based Office Climate Control System using wireless networks”** in the National Conference “NCRTCE-16” at Sree Sastha College of Engineering held on 9th Sep-2016.
- Participated and presented a paper titled **“Random Number Generation using Well Method”** in the “International Conference on Innovations and Challenges in Engineering and Technology-16” held at Kings engineering college on 9th April 2016.
- Participated and presented a paper titled **“Performance Evolution of Abdominal Image Fusion Based on Discrete Wavelet Transform”** in the National Conference “NCRTC-15” held at SreeSastha College of Engineering on 14th August-2015.

- Participated and presented a paper titled **“Design of High-performance Cyclic Redundancy Check”** in the two days National Conference held at Adhiyamaan College of Engg, Hosur, during 22nd & 23rd September-2011.
- Participated and presented a paper titled **“DCT implementation with Distributed Arithmetic”** in the National Conference Applications of Emerging Technologies held at Adhiyamaan College of Engineering, Hosur, on 28th March-2005.

Workshop:

- Participated in the two days workshop on **“Analog and Digital Design using Cadence tool”** organized by the department of Electronics and Communication Engineering at Dr.M.G.R Educational and Research institute, held on 4th & 5th March-2025.
- Participated in the one day workshop on **“SAKTHI RISC-V”** held as part of the DIR-V Symposium 2025 at IIT Madras Research Park on 01-03-2025.
- Participated in the National level workshop on **“Research Perspective on Full custom and Semi custom Design using Cadence EDA Tool ”** organized by the department of Electronics and Communication Engineering in association with Entuple Technology, IIC and IETE at SA Engineering College from 24-8-2023 to 25-8-2023.
- Participated in the National level workshop on **“Documenting Technical Reports using LaTeX”** organized by the department of Computer Science and Engineering, Sri Venkateswara College of Engineering from June 1, 2021 to June 5, 2021.
- Participated in One day Workshop on **“PCB DESIGN”** Organized by Department of ECE, Kings Engineering College in association with Crystal Clear Technology and Innovation on 19/06/2020.
- Participated in One day online Workshop on **“Intellectual Property Rights”** Organized by IPPO Conducted on 10th & 11th April-2020.
- Completed the PCT distance learning Course **“Introduction to the patent cooperation treaty”** by WIPO ACADAMY.
- Attended one day pre conference workshop on **“International Conference on smart securities and system”** held on 12th February 2018 at Saveetha Engineering College, Chennai.
- Attended 1 Day workshop on **“Persuasive Research Promotion Strategies”** held on 25th November 2017 at Kings Engineering College, Chennai.
- Attended two Days workshop on **“Embedded System Design and Real time applications”** conducted on 22nd & 23rd of August 2017 at Kings Engineering College, Chennai.
- Attended one Day workshop on **“Research Methodology”** held on 3rd December 2016 at Kings Engineering College, Chennai.

- Participated in two days workshop on **“Introduction to Robotics”** conducted on 22nd&23rdof February 2016 held at Sri Chandra sekharendra saraswathi viswa mahavidyalaya(SCSVMC) university, Kanchipuram.
- Attended one day Workshop on **“SOC and its signal integrity issues”** at VIT University on 31st October 2009.
- Attended two days National level Workshop on **“Digital Signal Processing, Pattern Recognition & Applications”** at National Engineering College, Kovilpatty on 10th& 11th January 2008.
- Attended one day workshop on**“National level Workshop on ARM9”atism Technology, Bangalore-10**,on 23rd November 2007.
- Attended the three days Workshop on **“VLSI Design Techniques”** Organized under Technical Education Quality Improvement Programme (TEQUIP) at Thanthai Periyar Govt. Inst. of Technology, Vellore, from 08 to 10-10-2007.

FDP and Seminar/Webinar:

- Participated and completed Faculty Development Program on **“Funding and Grants: Strategies for writing winning Project Proposal”**, Organized by the Department of Information Technology from “22-12-2025 to 27-12-2025 at Dr.M.G.R Educational and Research Institute, Chennai.
- Participated and completed AICTE Training and Learning (ATAL) Academy Faculty Development Program on **“SEMI-TECH: Semiconductor Innovation & Technology for Next Gen Electronics”**, from 18-08-2025 to 23-08-2025 at SRM Institute of Science and Technology, Kattankulathur.
- Participated in Six days online Faculty Development Programme (FDP) on **“Intelligent VLSI: The convergence of AI Signal Processing and Computing”** Organized by the Department of RF and Communication System, SIMATS Engineering, Chennai from 07-04-2024 to 12-04-2024.
- Participated and completed AICTE Training and Learning (ATAL) Academy online Faculty Development Program on **“Design for Test (DFT) Strategies in AI and Machine Learning SOCs”**, from 27/01/2025 to 01/02/2025 at DMI ENGINEERING COLLEGE, Chennai.
- Participated and completed AICTE Training and Learning (ATAL) Academy online Faculty Development Program on **“Advanced Research Trends for New Semiconductor Chip Designing”**, from 20/01/2025 to 25/01/2025 at SAVEETHA ENGINEERING COLLEGE, Chennai.
- Participated and completed AICTE Training and Learning (ATAL) Academy online Faculty Development Program on **“Unlocking Potential of AI in VLSI System Design**

and IC Packaging”, from 16/12/2024 to 21/12/2024 at B V RAJU INSTITUTE OF TECHNOLOGY.

- Participated in a outreach webinar on **“NEXT-GEN VLSI : TRENDS AND CAREER PATHWAYS”** organized by the Department of Electronics and Communication Engineering, SRM Institute of Science and Technology, Vadapalani Campus, Chennai, held on 11th December 2024.
- Attended Six days online Faculty Development Programme (FDP) on **“Next-Gen Educators: Harnessing the Power of Machine Learning”** Organized by the Department of Information Technology, from 25-09-2024 to 01-1-2024 at Jeppiaar Institute of Technology, Chennai.
- Participated and completed AICTE Training and Learning (ATAL) Academy offline Faculty Development Program on **“Micro to Nano- A journey for Inspiring Creations”**, from 19-08-2024 to 24-08-2024 at SA Engineering College, Chennai.
- Attended Six days online Faculty Development Programme (FDP) on **“Latest Development in RF Technologies and Beyond”** Organized by the Department of Information Technology, in association with IIC, REC & IEEE Communication Society from 31-07-2024 to 06-08-2024 at Rajalakshmi Engineering College, Chennai.
- Participated in a one day webinar on **“VLSI in AI/ML: Enabling Smart Technologies”** organized by Maven Silicon(Centre of Excellence in VLSI)-Bangalore on the 20th of June 2024.
- Participated and completed AICTE Training and Learning (ATAL) Academy Faculty Development Program on **“Trends in 5G networks – Applications in Biomedical engineering”**, from 18-12-2023 to 23-12-2023 at Ramco Institute of Technology, Rajapalayam.
- Participated and completed AICTE Training and Learning (ATAL) Academy Faculty Development Program on **“Silicon to Chip Design”**, from 11-12-2023 to 16-12-2023 at Vellore Institute of Technology, Vellore.
- Participated in one-week online FDP on **“VLSI and Chip Design”**, Conducted by Department of Electronics and Communication Engineering from 10th July to 14th July 2023 at Mohamed Sathak A.J. College of Engineering, Chennai.
- Participated in National level One week FDP(Online) on, **“Recent trends in Data Science for Engineering”**, Organized by Department of Information Technology, from 26th to 30th June-2023 at Chaitanya Bharathi Institute of Technology, Hyderabad, Telangana.

- Participated in two days FDP on **“The Role of Teachers in Outcome Based Education”** Organized by IQAC, from 27/06/2023 to 28/06/2023 at, Kings Engineering College, Chennai.
- Participated in three days FDP on **“Applications of Deep Learning Techniques to 5G and Beyond 5G Wireless Communication Technologies”** Organized by ECE Department from 19/09/2022 to 21/09/2022 at, Bapatla Engineering College, Bapatla.
- Attended One Week online Faculty Development Programme on **“Applications of Cognitive Radio Networks”** Organized by the Department of Electronics and Communication Engineering, in association with IETE from 01st to 06th April 2022 at Kings Engineering College, Chennai.
- Participated Six days FDP on **“Digital VLSI Design”** Organized by the Department of Electronics and Communication Engineering, in association with Entuple Technologies from 31st Jan to 05th February-2022 at Sri Venkateswara College of Engineering, Chennai.
- Participated Completed successfully in Five days online Elementary Faculty Development programme on, **" System on Chip Design and Testing"** Organized by Indian Institute of Information Technology Tiruchirappalli, under AICTE Training And Learning (ATAL) Academy from 22-11-2021 to 26-11-2021.
- Participated Completed successfully in Five days Faculty Development programme on, **"Magnetic Levitation"** Organized by Hindusthan Institute of Technology (HIT), Coimbatore, under AICTE Training And Learning (ATAL) Academy from 20-09-2021 to 24-09-2021.
- Attended the NAAC Sponsored One-day Online Seminar on **“Qualitative & Quantitative Metrics in RAAF of NAAC”** conducted on 17-Sep-2021 by Rajalakshmi Engineering College, Chennai.
- Participated in one-week online FDP on, **“Emerging Technologies of Opto-VLSI and its applications”** Organized by the Department of ECE, Meerut Institute of Engineering & Technology, Meerut held from 31st August to 4th September 2021.
- Participated in two days FDP on, **“Quality Expectation in NBA”**, organized by Kings Engineering College in association with AICTE - Margdarshan on 30.08.2021 and 31.08.2021.
- Participated Completed successfully in Five days online Elementary Faculty Development programme on, **" VLSI -IP DESIGN Approach to SRAM compiler design"** Organized by GM Institute of Technology, under AICTE Training And Learning (ATAL) Academy from 16-08-2021 to 20-08-2021.

- Participated in two days FDP on, **“Quality Expectation in NBA”**, organized by Kings Engineering College in association with AICTE - Margdarshan on 30.08.2021 and 31.08.2021.
- Participated in six days online International FDP on, **“Research Innovations and Emerging advances in Electrical Engineering”** Organized by EEE department from 14 to 19-06-2021 at Easwari Engineering College, Chennai.
- Participated in Two weeks AICTE Sponsored online FDP on, **“Responsibility of Higher education institutions under AICTE in Implementing Sustainable Development Goals”** organized by department of Electronics and Communication Engineering from 31-03-2021 to 17-04-2021 at Sairam Institute of Technology, Chennai.
- Participated in Three days Online FDP on, **“Unleash Your research Potential”** Organized by IQAC in Coordination with Research Center – department of CSE, during 24-06-2020 to 26-06-2020 at Adhiyamaan College of Engineering.
- Participated in Online FDP on, **“Effective Technical and review paper writing”** Organized by IQAC held on 23-06-2020 at Adhiyamaan College of Engineering.
- Participated in a one day webinar on **“Article Writing and Journal Publications”** organized by the Department of Electronics and Communication Engineering, Kings Engineering College on 17th June 2020.
- Attended and Successfully Completed Six days STTP on, **“Module-4 Circuit theory online STTP on Bloom’s Taxonomy-Higher order Thinking Skills in Electronics and Communication Engineering”** Organized by department of Electronics and Communication Engineering from 08-06-2020 to 12-06-2020 at Saveetha Engineering College.
- Participated in the online Webinar on, **“Semi-Custom IC Design Flow using Cadance”** Organized by Saveetha Engineering College, Presented by Entuple Technologies, Bangalore on 08-06-2020.
- Attended and Successfully Completed Six days STTP on, **“Module-3 MPMS & VLSI online STTP on Bloom’s Taxonomy-Higher order Thinking Skills in Electronics and Communication Engineering”** Organized by department of Electronics and Communication Engineering from 01-06-2020 to 05-06-2020 at Saveetha Engineering College.
- Attended and Successfully Completed Six days STTP on, **“Module-2 Digital Electronics online STTP on Bloom’s Taxonomy-Higher order Thinking Skills in Electronics and Communication Engineering”** Organized by department of Electronics and Communication Engineering from 25-05-2020 to 29-05-2020 at Saveetha Engineering College.

- Participated in a 5-Day-National Level Online Faculty Development Program on, **“Artificial Intelligence”** from 22nd May 2020 to 26th May 2020 organised by the Department of Information Technology-Christian College of Engineering and Technology, in association with National Youth Council of India and Brainovision Solutions India Pvt. Ltd.
- Attended and Successfully Completed Six days STTP on, **“Module-1 Electronic Devices online STTP on Bloom’s Taxonomy-Higher order Thinking Skills in Electronics and Communication Engineering”** Organized by department of Electronics and Communication Engineering from 18-05-2020 to 22-05-2020 at Saveetha Engineering College.
- Participated in the 5 Day Online Faculty Development Program on, **“IoT Trends to Drive Innovation for Business and Digital Technology”** organized by the Department of Electronics and Communication Engineering, Hindusthan Institute of Technology, Coimbatore from 11.05.2020 to 15.05.2020.
- Received **FDP certificate from NPTEL** on **“CMOS Digital VLSI Design”** for the 8 weeks course (February – April 2019) .
- Participated in five days faculty development program on **“REFERENCE MANAGEMENT & KNOWLEDGE ORGANIZATION FOR JOURNAL WRITING ”** Organized by Aalim Muhammed Salegh College Of Engineering, Chennai held from **07.01.2019 to 11.01.2019**.
- Participated in five days faculty development program on **“RESEARCH PAPER WRITING ON USING JOURNALING TOOLS”** Organized by Aalim Muhammed Salegh College Of Engineering, Chennai held from **01.11.2018 to 05.11.2018**
- Received **FDP certificate from NPTEL** on **“Switching Circuits and Logic Design”** for the 12 weeks course (July – October 2018).
- Participated in ICMR Sponsored one day national level seminar on **“Impact of Electronic nose technology and its applications”**Conducted on 10-10-2017 at Hindusthan Institute of Technology, Coimbatore.
- Attended the Two Days Faculty Development Programme on**“FACTO2K17 (Funding, Authorizing, Counselling, TQM and Opportunities in Enterpreurship)”** held on 2nd and 3rd August 2017 at Sri Sairam College of Engineering, Bangalore.
- Participated in the NAAC Awareness Seminar/Training on **“Quality System in Higher Education”**during 29th& 30th September 2016 Organized by IQAC at Anna University, Chennai.
- AttendedFive Days Faculty Development Programme on**“Recent Trends & Tools in Modern wireless & Digital Communication Networks”**during 27-06-2016 to

01-07-2016 held at Sri Chandrasekharendra saraswathi viswa mahavidyalaya(SCSVMC) university, Kanchipuram.

- Attended the Two Days Faculty Development Programme on **“Building Embedded Systems with ARM Cortex- M MCU’s”** from 29th October to 30th October 2015 at Saveetha Engineering College, Chennai.
- Attended six days AICTE Sponsored FDP on **“Advanced VLSI Technology”** at National Engineering College, Kovilpatty during 15th to 20th July-2013.
- Attended one day Faculty Development Programme on **“Low Power VLSI Design”** at Veltech Dr.Rangarajan Dr.Sagunthala Engineering College, Chennai held on 13th October-2012.
- Attended AICTE-ISTE Sponsored Staff Development Programme on **“VLSI Design and System on Chip”** at Adhiyamaan College of Engineering, Hosur, from 8th to 22nd August 2011.
- Attended AICTE-ISTE Sponsored STTP on **“VLSI Design and its applications”** at Adhiyamaan College of Engineering, Hosur, from 16-06-2008 to 20-06-2008.
- Attended the FDP Program on **“VLSI Design”** at College of Engg, Guindy Campus, Anna University, Chennai, from 24-05-08 to 30-05-08.
- Attended the two days FDP on **“VLSI Laboratory equipments”** Organized under Technical Education Quality Improvement Programme (TEQUIP) at Salem GCE, from 12-07-2007 to 13-07-2007.

PROFESSIONAL MEMBERSHIPS:

- IAENG

OTHER DETAILS (If any):

- Acting as Anna University approved DC Member for Research Scholar.