Faculty Profile



Ms Sharmila J, M.E.,
Assistant Professor
Date of Joining:
01.12.2023

HIGHLIGHTS:

- Number of Journal Publications: 02
- Number of Conference Publications: 05
- H-Index: 0
- Project Funding Received: Nil
- Patents Published: 1
- Patents Granted: Nil

PROFESSIONAL LINKS:

- Google Scholar ID:https://ieeexplore.ieee.org/author/411238227314892
- Anna University Faculty ID: 2669291999
- AICTE Faculty ID: 1-43698754229
- Anna University Scholar ID: 24249997283
- LinkedIn: linkedin.com/in/sharmila-j-0217701a3

PROFESSIONAL BACKGROUND:

- Teaching Experience till date: 3 years
- Industrial Experience: Nil

INTERNATIONAL EXPOSURE:

- Paper Publish in ICATEM -Dubai

AREA OF SPECIALIZATION:

- VLSI & Design
- Antenna and Microwave engineering and related projects
- Electromagnetic theory and Transmission lines

PATENTS PUBLISHED/GRANTED:

- Virtual Brain: Model-Based Framework For Dependable Eeg Sensing And Actuation In Intelligent Brain Iot System

CONFERENCES/SEMINARS/WORKSHOPS ATTENDED:

- An Idea on Chip to start u program Recent FPGA and Cadence EDA tools
- 6 days FDP on VLSI Testing and Design for testability (15-07-2024 to 22-07-2024)
- 5 days FDP on National Level Workshop on recent EDA tools and methodologies for efficient VLSI circuit design (15-04-2024 To 22-04-2024)
- 5 days FDP on VLSI design flow: From RTL to GDSII (01-12-2025 To 06-12-2025)
- 5 days FDP on Advanced Research Tools for Electronic & Photonic System Design (08-12-2025 To 12-12-2025)
- 5 days FDP on Embedded Systems and IOT (06-07-2026 To 10-07-2026)
- 3 Days FDP on TCL conducted by PALS - IIT Madras (08-10-2026 To 10-10-2026)

CONFERENCES PUBLICATIONS

- Architectural Optimization of a VLSI SPED Sensor For Real Time Image Processing
- **2025 IEEE International Conference on Communication Networks and Computing (CNC)**
- Radiation-Hardened Reconfigurable FPGA Architecture Using Triple-Mode Redundancy and Error-Resilient Logic Blocks for Space Applications
- **2025 1st International Conference on Advancement in Futuristic Technologies (ICAFT)**
- A High-Performance Architectural Design for FIR Filtering in Fixed and Reconfigurable Applications
- **2025 5th International Conference on Trends in Material Science and Inventive Materials (ICTMIM)**
- Design and Implementation of Sped Technique using Hybrid Adder for Area Reduction
- **2024 International Conference on Sustainable Communication Networks and Application (ICSCNA)**
- An IoT-Based Power Monitoring Solution with Real-Time Data Display
- **2024 9th International Conference on Communication and Electronics Systems (ICCES)**